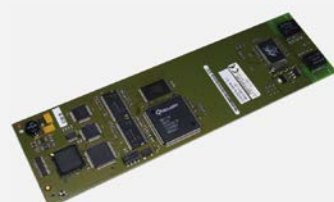


5.5.8.5 Technical Specification SM-2551 and SM-0551

SM-2551

Communication Circuits	
2 serial interfaces	Interface characteristics, interface signals, operation modes, transmission rates see description of the respective protocol, "Modes" • Byte-asynchronous or byte-isochronous pulse code modulation • Instead of the interface signal DSR the gate circuit voltage (+5 VDC) can be provided (settable) • Ability to be connected in parallel – Outgoing interface signals in tristate technology – Up to 2 interfaces can be connected in parallel – Circuits for incoming interface signals always active • The signals are galvanically insulated from logic circuits • Interfaces are galvanically insulated from each other • Line lengths V.28 – 50 bit/s up to 25 m – 115200 bit/s up to 5 m
Power Supply	
Operating voltage	4.75...5.25 VDC, typ. 300 mA, max. 540 mA at 5 V The voltage is supplied by the carrier module.
Gate circuit voltage +5 VDC instead of DSR	4.7...5.6 VDC The voltage is supplied by the carrier module, galvanically insulated • Max. output current 150 mA at $U > 4.75\text{ V}$ • Max. output power 750 mW • Max. idle voltage $\leq 5.6\text{ VDC}$ • Not short-circuit proof • Not overload proof • Galvanically insulated from logic voltage
Mechanics	
Dimensions	227.3 x 63.5 mm
Weight	Approx. 200 g

A.4 Serial Interface Modules

	Designation SM-2551 Serial interface processor 2 interfaces	Item-Number/MLFB BC2-551 6MF10130CF510AA0
	SM-2557 Network interface Ethernet 2x 100TX	BC2-557 6MF10130CF570AA0
	SM-2558 Ethernet-Interf. 1x100TX, +1 serial interface optional	BC2-558 6MF10130CF580AA0
	SM-0551 Serial interface processor 1 interface for SM-2558	BC0-551 6MF10130AF510A00