

SIMATIC control systems

SIMATIC TDC multiprocessor control system

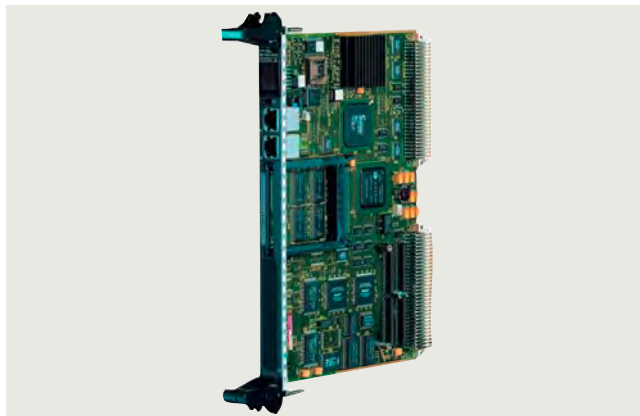
CPU555, CPU551 processor modules

Overview CPU555



- Graphic freely configurable processor module
- For implementing highly dynamic open and closed-loop control functions

Overview CPU551



High-performance CPU module for open and closed-loop control and arithmetic tasks.

Ordering data

Article No.

CPU555 processor module	6DD1600-0BB0
Accessories	
SIMATIC Micro Memory Card	
2 MB	6ES7953-8LL31-0AA0
4 MB	6ES7953-8LM32-0AA0
8 MB	6ES7953-8LP31-0AA0
Crossed twisted pair cables 4x2 with RJ45 connectors	
0.5 m	6XV1870-3RE50
1 m	6XV1870-3RH10
2 m	6XV1870-3RH20
6 m	6XV1870-3RH60
10 m	6XV1870-3RN10

Ordering data

Article No.

CPU551 processor module	6DD1600-0BA3
Accessories	
MC500 memory module (4 MB)	6DD1610-0AH4
MC510 memory module (8 MB)	6DD1610-0AH6
MC521 memory module (2 MB)	6DD1610-0AH3

Technical specifications

CPU551	
Required space / width	1 slot
Weight	0.6 kg
Display	5x7 LED
Local service interface	Serial RS232 interface
Sampling intervals	from 100 µs
SDRAM	128 MB
Synchronous cache	8 MB
Clock frequency	500 MHz
CPU	64 bit RISC CPU with floating point unit
SRAM	512 KB, battery buffered
Power supply	
Voltage / Power supply (at 25 °C)	+3.3 V, 2.0 A typically +5 V, 1.5 A typically +12 V, 0.04 A typically -12 V, 0.04 A typically
Buffer battery	3.0 V, 3 µA typically
Power loss, typically	15 W
Digital inputs	
Number	8 inputs, 4 with alarm capability
Galvanic isolation	Only through optional interface modules
Input voltage	24 V
• Nominal voltage	-1 V ... +6 V
• For 0 signal	+13.5 V ... +33 V
• For 1 signal	
Input power	0 mA
• With 0 signal	3 mA
• With 1 signal	
Delay time	100 µs
Real-time clock, resolution	0.1 ms

SIMATIC control systems

SIMATIC TDC multiprocessor control system

MC5xx program memory module, CP50M1 communications module

Overview MC5xx program memory module

Program memory module for the program designed with CFC.

Ordering data

Article No.

MC500 memory module (4 MB)	6DD1610-0AH4
MC510 memory module (8 MB)	6DD1610-0AH6
MC521 memory module (2 MB)	6DD1610-0AH3

Overview CP50M1 communications module



The CP50M1 communications module provides two PROFIBUS DP/MPI interfaces and an 8 MB interprocessor memory for inter-CPU communication. The interfaces can be used as PROFIBUS DP master, slave, as master and slave simultaneously or as MPI node.

Ordering data

Article No.

CP50M1 communications module	6DD1661-0AD1
------------------------------	--------------

Technical specifications

Power supply	
Voltage / Power supply	+5 V, 1.0 A typically
Power loss, typically	5 W
Required space / width	1 slot
Weight	0.34 kg