B&R - X20 Compact-S PLC

X20CP0484

Programmable Logic Controller CPU

Product identity and technical data

Processor	ARM Cortex-A9, 667 MHz, with integrated I/O processor
Working memory	256 MB DDR3 SDRAM
Application memory	2 GB eMMC flash memory
Remanent variables	64 kB FRAM, retention greater than 10 years
Shortest task class cycle	0.4 ms
Typical instruction cycle	0.0106 microseconds
Ethernet	1 x shielded RJ45 Ethernet, 10/100 Mbit/s, 100 m maximum segment length
POWERLINK	1 x shielded RJ45 POWERLINK V2, managing or controlled node, 100 Mbit/s, Type 6
USB	2 x USB 1.1/2.0 Type A, 0.2 A maximum output current per interface
Interface module expansion	0 slots with X20BB5x, 1 slot with X20BB6x, or 2 slots with X20BB7x
Power consumption	2.95 W
Operating temperature	-25 to 60 °C horizontal mounting; -25 to 50 °C vertical mounting

Documented role

X20CP0484 is a B&R programmable logic controller CPU for the X20 Compact-S family within the X20 System.

The unit provides ARM Cortex-A9, 667 MHz, with integrated I/O processor, 256 MB DDR3 SDRAM, 2 GB eMMC flash memory, Ethernet, POWERLINK V2, two USB interfaces, an X2X Link master, and bus-base-dependent serial and interface expansion.

Documented applications

- Machine-control applications scaled to the X20 Compact-S processing and memory class
- ACOPOS motion-control applications supported by the controller
- Visual Components machine-visualization applications supported by the controller

Selection and installation checks

X20CP0484 requires a compatible Compact-S bus base, X20PS9600 or X20PS9602 power supply, and X20TB12 terminal block; verify Automation Runtime, network roles, base-module interface options, 24 VDC supply, mounting orientation, and the saved project before installation or replacement.

Degree of protection: IP20; relative humidity: 5 to 95%, non-condensing.