

B&R - X20 System

X20CP1483

Programmable Logic Controller CPU

Product identity and technical data

Product family	X20 System
Product type	Programmable logic controller CPU
Processor	x86 100 compatible CPU, 100 MHz, with integrated I/O processor
Working memory	32 MB SDRAM
SRAM	128 kB SRAM
Remanent variables	32 kB maximum
Shortest task class cycle	1 millisecond
Typical instruction cycle	0.09 microseconds
Ethernet	1 x 10/100 Mbit/s Ethernet, shielded RJ45
POWERLINK	POWERLINK V1/V2, 100 Mbit/s, managing or controlled node
USB	2 x USB 1.1 Type A, 0.5 A maximum per port
Serial interface	RS232 via X20TB12, 115.2 kbit/s maximum, 900 m maximum

Documented role

X20CP1483 is a B&R; X20 System programmable logic controller CPU for industrial automation control.

The fanless controller provides x86 100 compatible CPU, 100 MHz, with integrated I/O processor, 32 MB SDRAM, integrated Ethernet, POWERLINK, USB, RS232, X2X Link master, and removable CompactFlash application storage.

Selection and installation checks

- Uses x86 100 compatible CPU, 100 MHz, with integrated I/O processor
- Provides 32 MB SDRAM, 1 x 10/100 Mbit/s Ethernet, shielded RJ45, and POWERLINK V1/V2, 100 Mbit/s, managing or controlled node
- Runs fanless with a shortest task class cycle of 1 millisecond

X20CP1483 requires separately ordered CompactFlash application memory; verify the material number, runtime and project, interface modules, network roles, 24 VDC power segmentation, USB loads, mounting orientation, and validated backup before replacement.