

B&R · B&R System 2003

7CP476.60-1

System 2003 CPU with CAN and System Bus

Technical specifications

SRAM	750 kB
User FlashPROM	1.5 MB
Nominal input voltage	24 VDC
Input voltage range	18 to 30 VDC
Serial	1 RS232 interface
Power consumption	20 W
Output power for I/O ports	12.5 W
Average instruction cycle time	0.5 µs
System FlashPROM	512 kB
RS232 connector	9-pin male D-sub
RS232 maximum transfer rate	57.6 kbit/s
Maximum logical module slots	16
Maximum analog module slots	4
Analog module addresses	1 to 8
Operating temperature	0 to 60°C horizontal; 0 to 50°C vertical
Storage temperature	-25 to 60°C
Relative humidity	5 to 95%, non-condensing

Description

B&R 7CP476.60-1 is a System 2003 CPU with 750 kB SRAM, 1.5 MB FlashPROM, isolated CAN networking and a system bus for expansion modules.

It controls a System 2003 station, accepts four screw-in modules and extends the station through the documented system bus.

Before installation or replacement, verify the 24 VDC supply, CAN node and termination, system-bus expansion order, screw-in modules, saved application and Automation Runtime compatibility.

Applications

- System 2003 machine control with local screw-in I/O and interface modules
- Distributed machine control over an electrically isolated CAN network
- Expanded PLC stations using the System 2003 system bus

Selection and replacement checks

B&R 7CP476.60-1 must be matched by complete material number, compatible System 2003 CPU or station, slot and terminal map, power and field wiring, saved project and connected devices. Back up the configuration and perform a controlled functional test after replacement.