

B&R · B&R System 2005

3CP260.60-1

System 2005 PLC CPU with PCMCIA and Interface Slots

Exact-model official B&R product evidence found

Technical specifications

Processor class	System 2005 CPU
DRAM	4 MB
System SRAM	174 kB SRAM
User SRAM	850 kB
System FlashPROM	512 kB
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Power consumption	5 V: max. 5.7 W; 24 V: max. 2.3 W; total max. 8 W
Interface insert slots	2
PCMCIA slots	1
Serial interface	1 x RS232
Installation position	Immediately beside the power supply
Module footprint	Occupies 2 System 2005 slots
Supported memory cards	2 MB / 4 MB FlashPROM or 2 MB SRAM PCMCIA options
Certifications	CE, UKCA, cULus E115267
Dimensions (H x W x D)	16.5 x 8.0 x 11.0 cm

Documented role

B&R 3CP260.60-1 is a System 2005 PLC CPU with 4 MB DRAM, 850 kB SRAM, 512 kB FlashPROM, two interface-module slots and one PCMCIA memory-card slot.

It executes System 2005 machine-control applications and can combine interface inserts for different bus or network systems while using removable PCMCIA media for field program transfer.

Applications

- System 2005 PLC control with configurable fieldbus or network inserts
- Machine applications requiring removable PCMCIA program transfer
- Service replacement of an installed CPU after restoring the verified project and interface configuration

Selection note

B&R 3CP260.60-1 must be matched by complete material number, compatible System 2005 station or CPU, slot and terminal map, power and field wiring, saved project and connected devices. Back up the configuration and perform a controlled functional test after replacement.