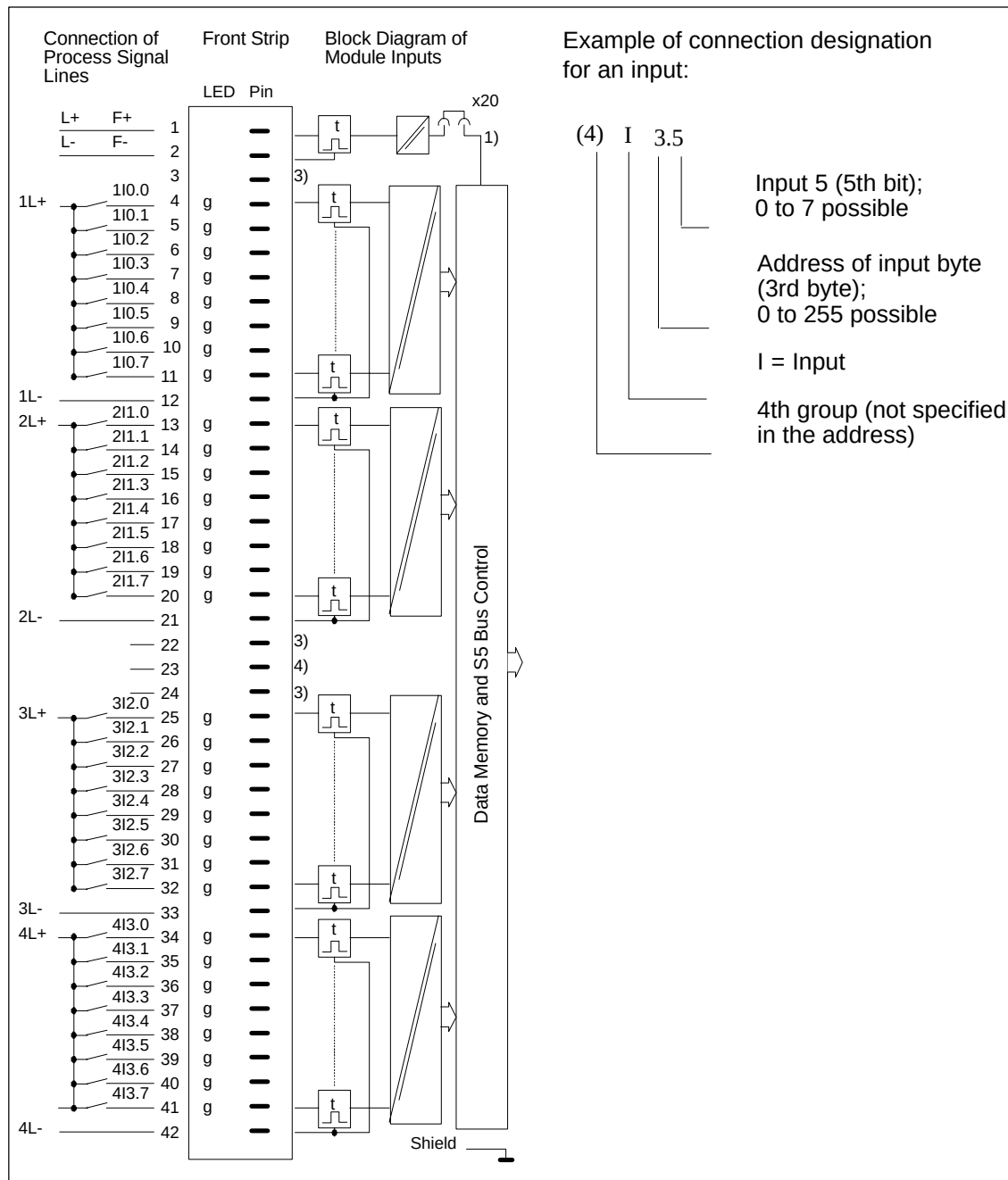


8.4.4 6ES5 432-4UA12 Digital Input Module

Rated input voltage	24 V DC
Number of inputs	32
Isolation	Yes, 4 groups of 8 inputs
Input voltage for logic 0 for logic 1	-33 to 5 V 13 to 33 V
Rated input current	8.5 mA
Input frequency	100 Hz / 300 Hz / 1 kHz max.
Delay time ¹⁾	3 ms / 1 ms / 0.3 ms typical (1.5 to 4.8 ms/0.5 to 1.6 ms/0.15 to 0.48 ms)
Input resistance	2.8 kohm typical
Coincidence factor (total load capability)	100 %
Permissible line length	600 m max. unshielded (3 ms); 200 m max. unshielded (1 ms); 50 m max. unshielded (0.3 ms)
Power supply	
Digital section from system bus	5 V, 130 mA typical
Supply voltage for 2-wire BERO	22 to 33 V
Power dissipation (rated operation)	7.5 W
Enable input (F+/F-)	
Rated input voltage	24 V DC
Input voltage for logic 0 for logic 1	-33 to 5 V 13 to 33 V
Rated input current	5 mA
Permissible line length	200 m max.
Voltage test to VDE 0160	Between two groups: 500 V AC Between group and ground point: 500 V AC
Mechanical specifications	
Dimensions (W x H x D)	20 mm x 255 mm x 195 mm
Weight	Approx. 0.4 kg

¹⁾ Selectable in bytes with switch S5



g = Green LED (status indicator)
F+/F- = Enable input

- 1) Changeover of enable mode with jumper X20:
Jumper inserted = Enable input active (factory setting)
Jumper open = Enable input inactive.
- 2) Only 128 to 255 permissible with group signal and interrupt.
- 3) The terminal is not connected internally. When this terminal is connected to the input voltages, the clearances in air and leakage paths are no longer adequate to UL and CSA, but comply with VDE.
- 4) The terminal is not connected internally. When this terminal is connected to the input voltages, the clearances in air and leakage paths remain adequate to UL, CSA and VDE.

Labeling for module cover:

Mark the switch settings in the free fields.

	X3
Master (on)	
Slave (off)	

Delay-Time					S5
Byte	3 ms	1 ms	0.3 ms		
3	•	•	•		
	•	•	•		
2	•	•	•		
	•	•	•		
1	•	•	•		
	•	•	•		
0	•	•	•		
	•	•	•		

X4	
Group Signal	
active (on)	
inactive (off)	

Edge Trigger					S3
Byte	↑	↓	↑	↓	
3	•	•	•	•	
	•	•	•	•	
2	•	•	•	•	
	•	•	•	•	
1	•	•	•	•	
	•	•	•	•	
0	•	•	•	•	
	•	•	•	•	
Byte	↑	↓	↑	↓	

S1	Interrupt
	IRA
	IRB
	IRC
	IRD
	IRE
	IRF
	IRG
	INT

Group Signal																	S2
Data bit	Master submodule (Jumper X3 on)								Slave submodule (Jumper X3 off)								
	without Slave	1 Slave	2 Slave	3 Slave	4 Slave	5 Slave	6 Slave	7 Slave	1.Slave o.1	2.Slave 0.2	3.Slave 0.3	4.Slave 0.4	5.Slave 0.5	6.Slave 0.6	7.Slave 0.7		
0	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	
1	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	
2	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	
3	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	
4	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	
5	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	
6	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	
7	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	●	